

Fig. 1

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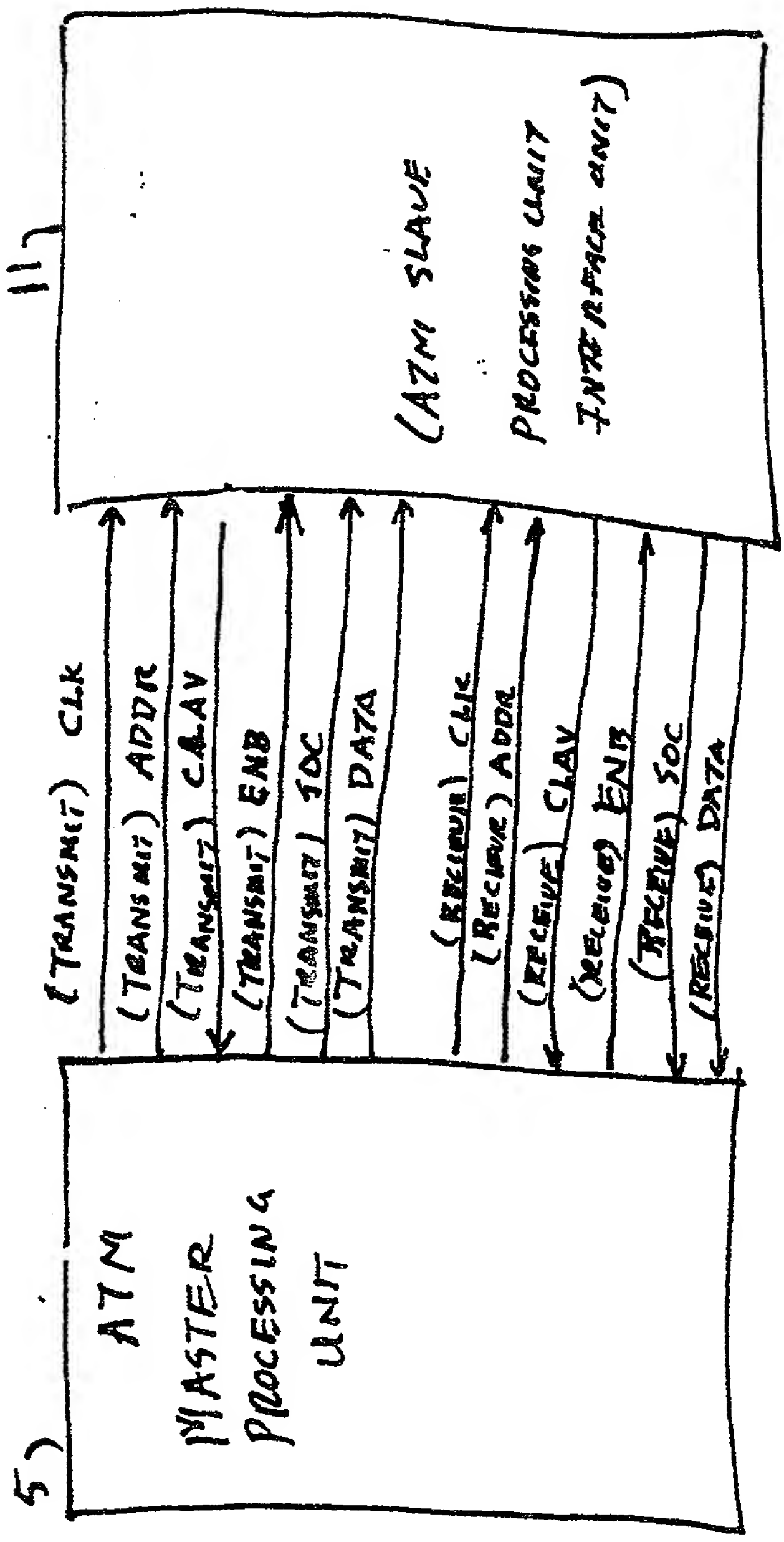


Fig. 2

FIG. 3 is a block diagram of a system for providing a direct memory access unit to a slave unit.

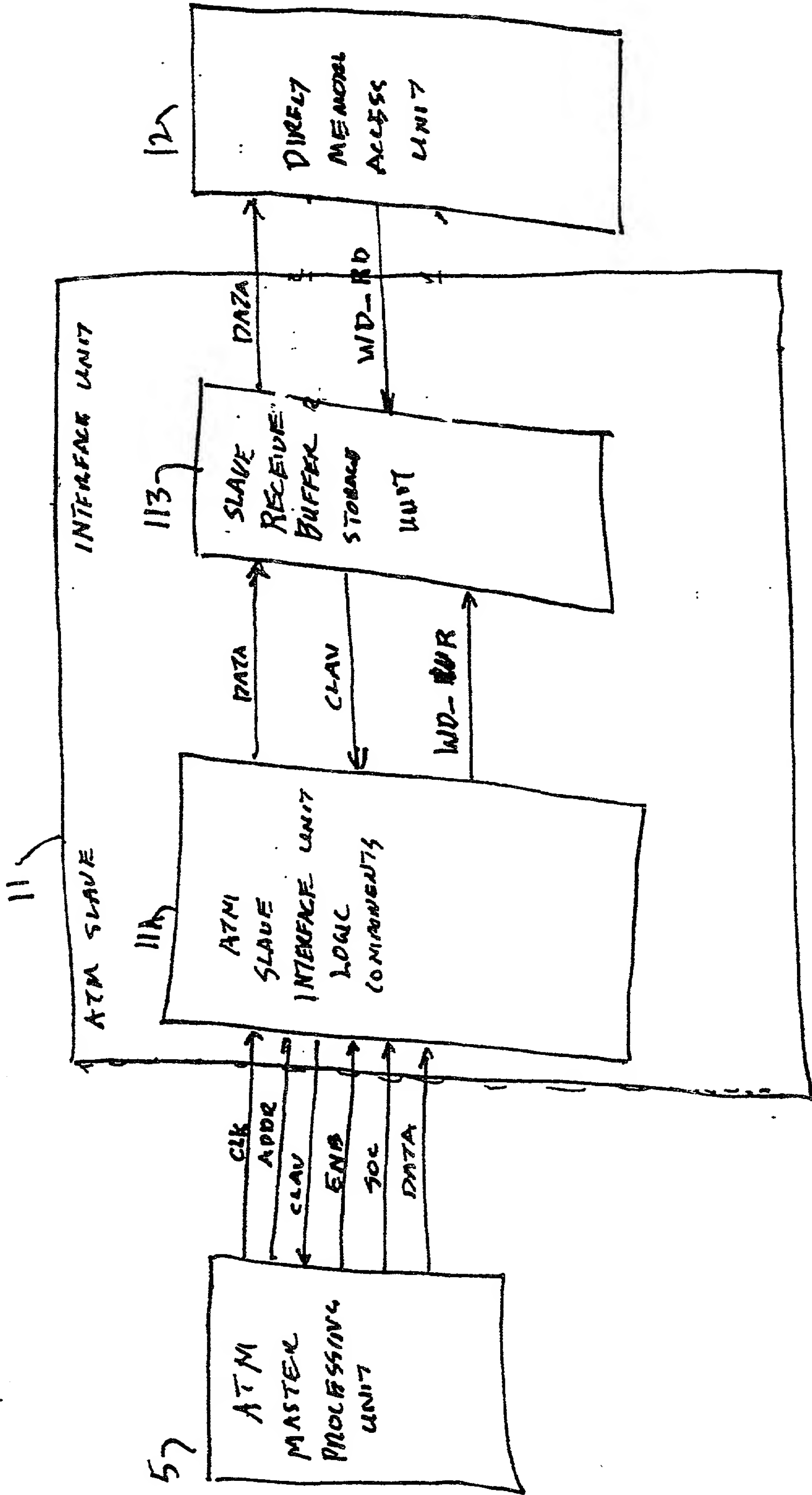


Fig. 3

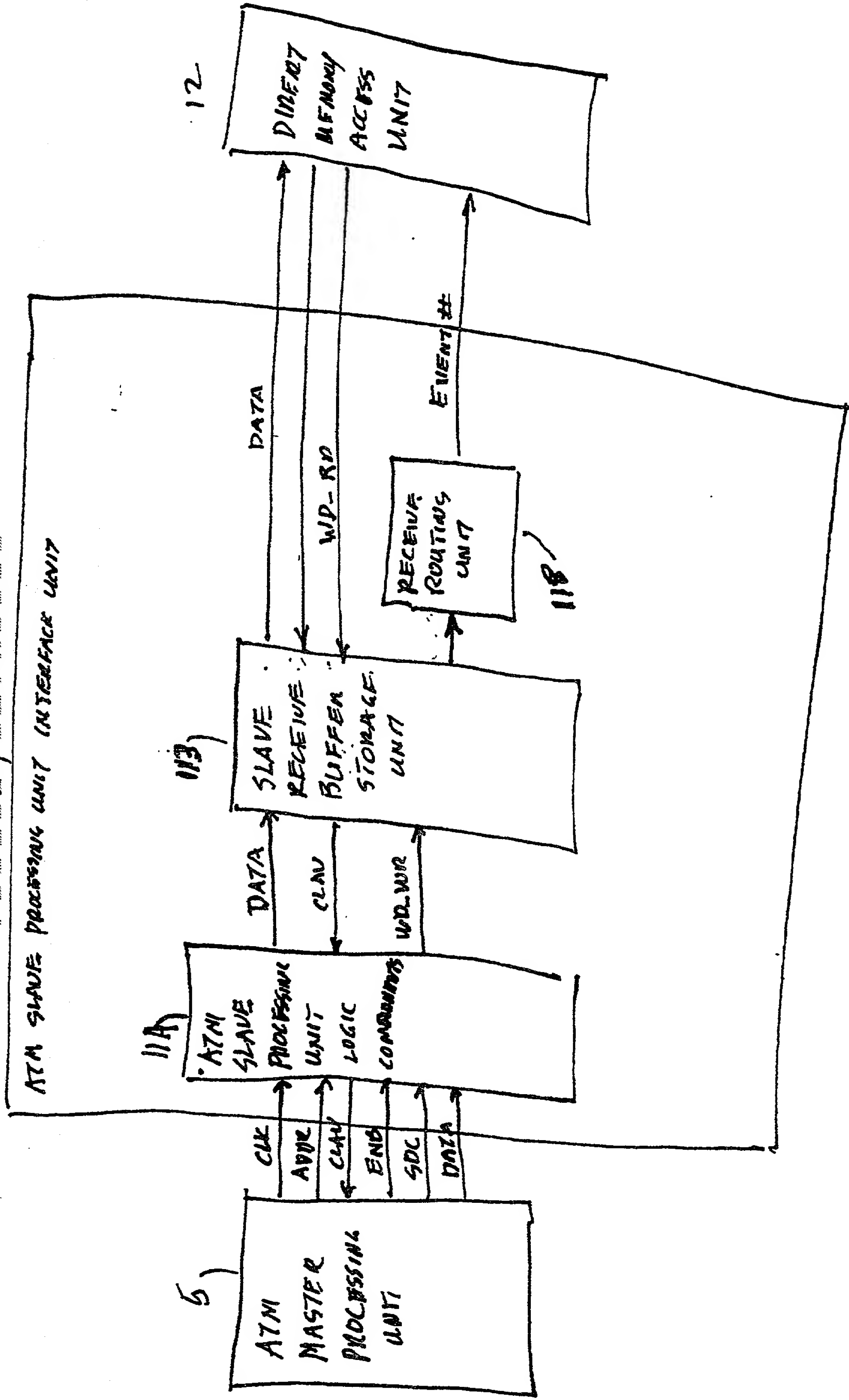


Fig. 4

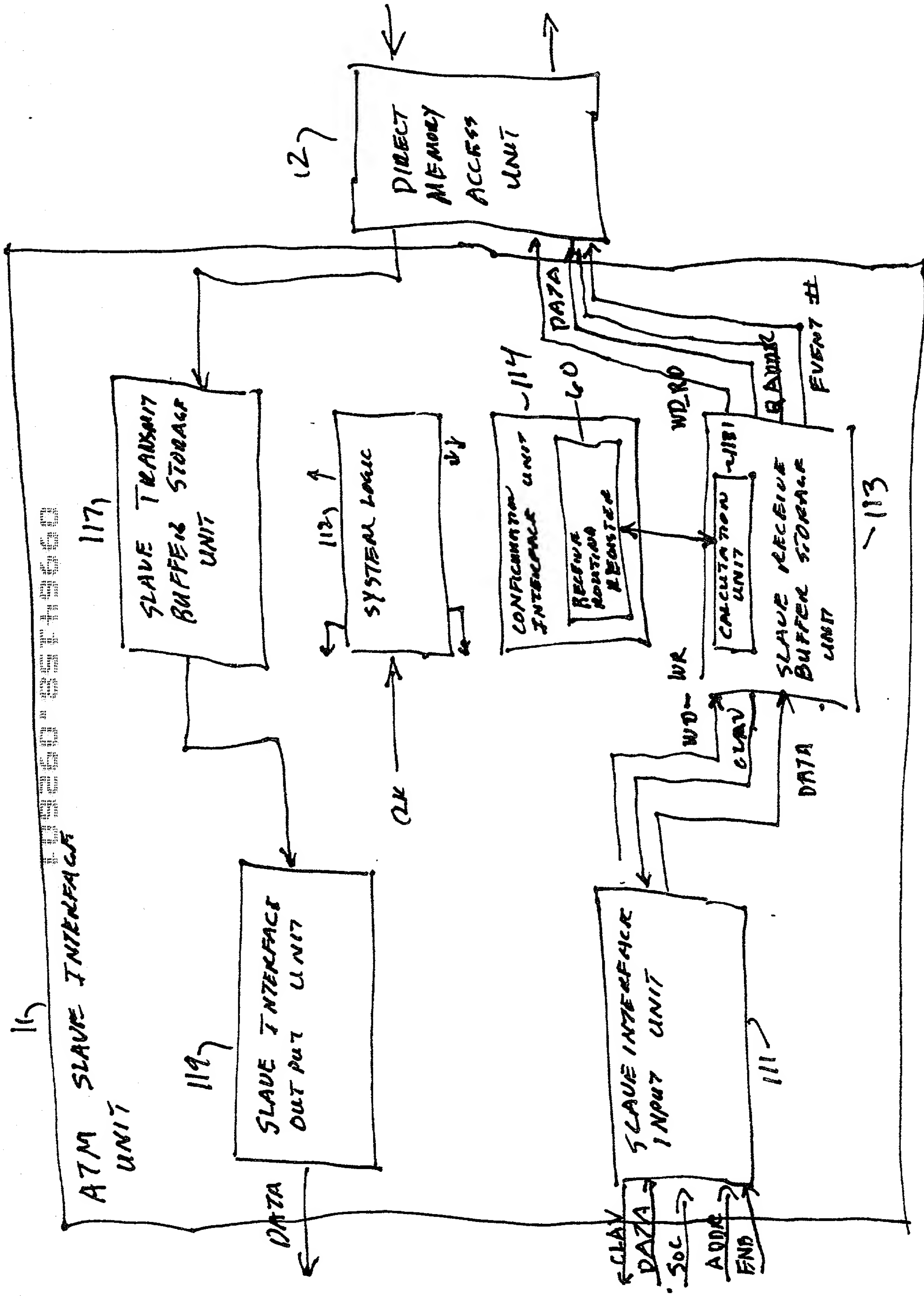


Fig. 5

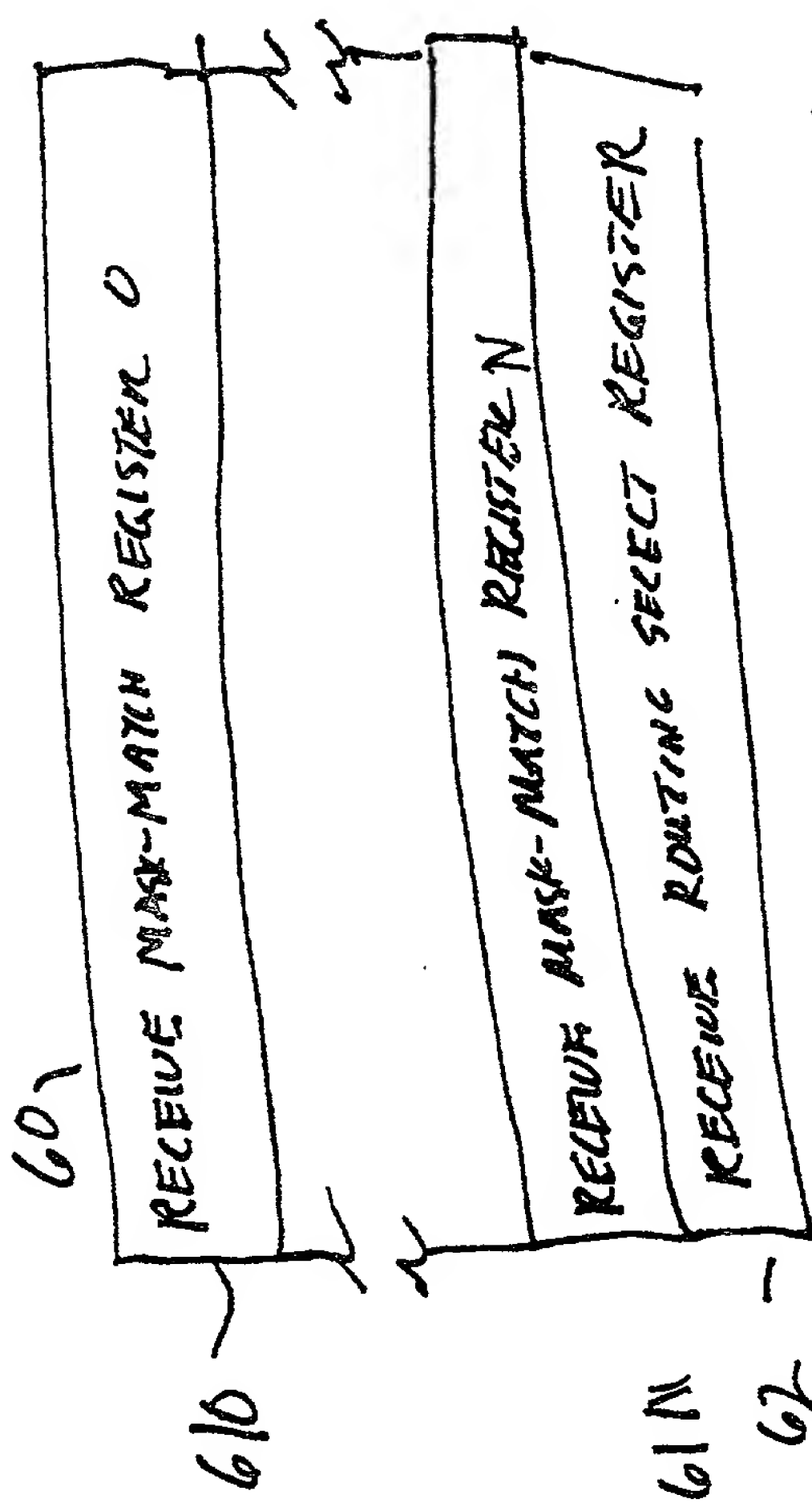


Fig. 6